

Notice of References Cited	Application/Control No. 10/762,781	Applicant(s)/Patent Under Reexamination ZHANG ET AL.	
	Examiner Brandon W. Bowers	Art Unit 2825	Page 1 of 1

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	V	Long, "Logic Design Principles and Examples", VLSI Handbook, Ch. 71, CRC/IEEE Press, 2000
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.